L Number	Hits	Search Text	DB	Time stamp
-	609	"delay circuit" with clock same "data signal"	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/05/22 14:00
-	1194	(365/194).CCLS.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/05/22 14:01
-	2202	(365/189.05).CCLS.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/05/22 14:01
-	2353	(365/233).CCLS.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/05/22 14:01
-	7150	(delay or delayed) with data with buffer	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/05/22 14:02
-	67	("delay circuit" with clock same "data signal") same buffer	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/05/22 14:07
-	83	input near3 data near3 buffer same clock same delay same "delay circuit"	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/05/22 14:38
-	127	input near3 data near3 buffer same (delay delayed delaying) near3 clock	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/05/22 14:13
-	1	"delay information" same "delay circuit" same clock same buffer	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/05/22 14:40
-	2162	"delay information"	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/05/22 14:41
-	12	"delay buffer" same clock same data same (selective selecting select)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/05/24 10:28
-	382	(programmable controllable) with delay with buffer	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/05/24 10:54
-	1429	(programmable controllable variable) adj3 delay same buffer	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/05/24 10:56
-	1222	(programmable controllable variable) adj delay same buffer	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/05/24 10:58

-	302	((programmable controllable variable) adj delay same buffer) same clock near3 (delay delaying delayed)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/05/24 13:46
-	6	("4739277" | "5255383" | "5577236" | "5608896" | "5812472" | "5815016").PN.	USPAT	2003/05/24 13:47
-	2	"memory module" same "register" same clock same PLL same delay	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/05/27 09:36
-	23	"memory module" and "register" same clock same PLL same delay	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/05/27 09:39
-	83	"memory module" same "delay circuit"	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/05/27 10:32
-	187	"memory controller" same "latch" same delay	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB.	2003/05/27 11:09
-	1511	yoshimori.in.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/05/27 11:09
-	35	yoshimori.in. and integrated.ti.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/05/27 11:44
-	744	DLL with PLL	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/05/27 11:44
-	478	DLL near3 PLL	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/05/27 11:44
-	454	(DLL near3 PLL) and clock	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/05/27 11:45
-	352	(DLL near3 PLL) same clock	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/05/27 11:45
-	210	(DLL near3 PLL) same clock same delay	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/05/27 14:46
-	216	DLL.ti.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/05/27 11:52
-	709	(delay and locked and loop).ti.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/05/27 11:56

-	157	(delay and locked and loop).ti.	USPAT	2003/05/27 13:39
-	66	(delay and locked and loop).ti. and PLL	USPAT	2003/05/27 13:40
-	5	"delayed" near3 "phase-lock"	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2003/05/27 14:46